Indian Institute of Information Technology Bhagalpur Computer Science and Engineering (CSE)

B.Tech. Course Curricula and Syllabus

Semester-II

Curricula:

Course Code	Course name	L	т	Ρ	С
<u>MA102</u>	Engineering Mathematics – II	3	1	0	4
<u>CS102</u>	Data Structures and Algorithms	3	0	0	3
<u>EC102</u>	Digital Design	3	0	0	3
<u>EC104</u>	Semiconductor Devices and Circuits	3	0	0	3
<u>ME103</u>	Engineering Materials	3	1	0	4
CS112	Data Structure and Algorithm LAB	0	0	3	2
EC113	Digital Design LAB	0	0	3	2
EC114	Semiconductor Devices and Circuits LAB	0	0	3	2
NSS	National Service Scheme	0	0	0	0

Syllabus:

Course Code	Course Name	L	Т	Ρ	С	Year	Semester		
MA102	Engineering Mathematics II	3	1	0	4				
Course Obj	ective:								
Торіс									
Module I	Vector functions of one variable – continuity, differentiation and integration. Functions of several variables - continuity, partial derivatives, directional derivatives, gradient, differentiability, chain rule; tangent planes and normals. Concavity-Convexity, Maxima and minima, Saddle Pont, Lagrange multiplier method.								
Module II	Repeated and Multiple integrals with applications to volume, surface area, Moments of Inertia, change of variables, Vector Fields, Line and Surface Integrals.								
Module III	Green's, Gauss' and Stokes' theorems and their applications. Module III First order differential equations - exact differential equations, integrating factors, Bernoulli equations, existence and uniqueness theorem, applications.								
Module IV	Higher-order linear differential equations, solutions of homogeneous and non- homogeneous equations, method of variation of parameters. Series solutions of linear differential equations. Legendre equation and Legendre polynomials. Bessel equation and Bessel functions of first and second kinds.								
Module V	Module V Systems of first-order equations, two-dimensional linear autonomous system, phase plane, critical points, stability.								
						Total	43		

Text	 Thomas Calculus; Maurice D Weir, Joel Hass, ; 13th, Pearson India Education Services Pvt.Ltd; 2008. Advanced Engineering Mathematics; Erwin Kreyszig, Herbert Kreyszig, Edward J Norminton; 10th, Wiley India Pvt. Ltd.; 2017. Elementary Differential Equations and Boundary Value Problems; William E Boyce, Richard C DiPrima, ; 9th, Wiley India Pvt. Ltd.; 2009.
Reference	 Calculus Early Transcendentals; James Stewart, , ; 7th, Cengage; 2012. A Course in Multivariable Calculus and Analysis; Sudhir R Ghorpade, Balmohan V Limaye, ; 1st, Springer; 2018. Differential Equations; Shepley L Ross, Richard C DiPrima, ; 3rd, Wiley India Pvt. Ltd.; 2004.

Course Code	Course Name	L	Т	Р	С	Year	Semester
CS102	Data Structures and Algorithms	3	0	0	3	1 st	2 nd
Course Objective: .	A good algorithm usually comes together	with	a se	t of į	good	data structu	res that allow
the algorithm to m	nanipulate the data efficiently. In this cour	se, s	tude	nts v	vill g	et to know v	arious data
structures that are	used in various computational problems.						1
Торіс							Hour
Module I	Performance of algorithms: space and tim lower and upper bounds.	ie co	mple	exity	, asy	mptotics,	7
Module II	Fundamental Data structures: arrays, linko queues, binary trees, tree traversals.	ed lis	sts, r	natri	ces,	stacks,	7
Module III	Algorithms for sorting and searching: linear search, binary search, insertion-sort, selection sort, bubble sort, quicksort, mergesort, heapsort; Priority Queues: lists, heaps.						7
Module IV	Hashing: separate chaining, linear probing, quadratic probing; Search Trees: binary search trees, B-trees.					ng;	6
Module V	Graphs: Data Structures for Graphs, Breadth First Search, Depth First Search.						8
						Total	35
 Data Structures with C; Seymour Lipschutz, ; 1st, McGraw Hill Education (India) Pvt. Limited; 2011. Introduction to Algorithms; Thomas H Cormen, Charles E Leiserson, Ronald L Rivest; Srd, PHI Learning Private Limited; 2018. 						on (India) Pvt. onald L Rivest;	
Reference	1. Fundamentals of Data Structures in C; E 2nd, Universities Press (India) Private Limi	Ellis I ted;	Horo 201	witz, 7.	, Sart	aj Sahni, Sus	an Anderson;

Course Code Course	name L	Т	Р	С	Year	Semester
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EC102	Digital Design	3	0	0	3	1 st	2 nd
Course objective system abstract sequential circui	umber system, logic gates, nine (FSMs).	elements of digital combinational and					
Торіс	Contents					. ,	No. of Lectures
Module-I	Module-I Number Systems and Codes, Positional number system, Binary, octal and hexadecimal number systems; Methods of base conversions; Binary, octal and hexadecimal arithmetic; Representation of signed numbers; Fixed and floating point numbers; Binary coded decimal codes; Gray codes; Error detection and correction codes - parity check codes and Hamming code						06
Module-II	Combinatorial Logic Systems: De- table; Basic logic operation and lo Switching Functions: Basic postulate Boolean algebra; Standard represe and POS forms; Simplification of s Quine-McCluskey tabular methods; circuits	finitio egic g es an entat witch Synt	on ar gates; d fun ion o ning f hesis	nd sp Boc Idam f log funct of co	pecifi enta ic fui ions ombi	cation; Truth Algebra and theorems of nctions - SOP - K-map and national logic	10
Module-III	Logic Gates, Two-level realizations using gates -AND-OR, OR-AND, NAND-NAND and NOR-NOR structures; Multifunction gates, Multi- bit adder, Multiplexers, DE-multiplexers, Decoders, Programmable ALU; Multiplexer-based realization of K-maps; Combinational circuit design using multiplexers and gates						09
Module-IV	Module-IV Sequential Logic systems: Latches and Flip-flops, Timing hazards and races; Analysis of state machines using D flip-flops and JK flip-flops; Synchronous and Asynchronous counters; Registers; Sequence generator using flip-flops; Design of state machines-state table, state assignment, transition/excitation table, excitation maps and equations, logic realization; Design examples						10
Memory:Read-only memory, read/write memory-SRAM and DRAM; Module-V TTL, MOS, interfacing between logic families; RAM, ROM, PAL, and PLA.						07	
Total							42
 M. Morris Mano, Digital Logic and Computer Design, Pearson Edu 2009. R. P. Jain, Modern Digital Electronics. Tata McGraw Hill. 3rd editic 							ication, 11 th edition, n, 2011.
Reference	1. R. J. Tocci, N. S. Wisdmer and G Pearson Education, 10 th edition	. L. N , 201	1oss, 1.	Digit	al Sy:	stems: Principl	e and Applications,

Course Code	Course name	L	Т	Ρ	С	Year	Semester
EC104	Semiconductor Devices & Circuits	3	0	0	6	1 st	2 nd
Course objecti mechanism, s particularly, th 1. Introduce 2. Provide stu	nductor mate devices and rorking of semi uctor devices a	rials and transport transistors. More conductor devices. and technologies.					
Торіс	Contents						No. of Lectures
Module-I	Module-I Introduction of semiconductors, equilibrium and carrier concentration in semiconductors; Bond model and band model of intrinsic semiconductors, Density of state, Fermi-dirac distribution function; Carrier transport in semiconductors, Mobilty, resistivity and conductivity; Excess carrier, method of generating excess carrier inside extrinsic semiconductors. Doping and diffusion process.						
Module-II	Module-II P-N Junction: Simplified device structure and physical operation of diode; depletion region, forward and reverse-bias, depletion and diffusion capacitances, switching characteristics; breakdown mechanisms; Zener diode, Tunnel diode; Diode Applications: Half Wave and Full Wave Rectifier, Clippers and Clampers, and Zener Regulators						
Simplified device structure and physical operation of BJT, I-V characteristics of BJT, carrier distribution; current gain, transit time, secondary effects; SPICE model. Metal-semiconductor junctions, Breakdown of the junction with the non-impact and impact ionization, β-Ic characteristics curve, variation of α with Ic; Small signal equivalent circuit, BJT Amplifiers: Transistor Configuration analysis, Common base, Common emitter and Common collector							08
MOS structure, Energy band diagrams, Flat-band condition and flat- band voltage, Surface accumulation, surface depletion, Threshold condition and threshold voltage, MOS C-V characteristics, MOS Q-V Characteristics.						08	
Module-V	09						
Total							42

	1.	R. F. Pierret, Semiconductor Device Fundamentals, Pearson Education, 1 st edition,
Text		2006.
	2.	B. G. Streetman and S. K. Banerjee, Solid State Electronic Devices, Pearson Education, 7 th edition, 2015.
Reference	1.	J. Singh, Semiconductor Devices - Basic Principles, John Wiley & Sons Inc., 1 st edition, 2001.

Course Code	Course name	L	Т	Ρ	С	Year	Semester
ME103	Engineering Materials	3	0	0	3	1 st	2 nd
Topic	Conter	No. of Lectures					
Module-I	Ile-I Ile-Ile-I Ile-Ile-Ile-Ile-Ile-Ile-Ile-Ile-Ile-Ile-						08
Module-II	Environmental Degradation of materials: Oxidation and Corrosion, Thermal and Photo Degradation, Chemical Degradation, Radiation Damage.						08
Module-III	Structure of solids: Crystalline and Non-crystalline states, Crystallographic directions and phases, Determination of crystal structures.					08	
Module-IV	Properties of materials: Thermal Properties, Electrical properties,IVDielectric behaviour, Magnetic properties, Superconductivity, Optimal properties.					08	
Module-V	Materials selection: Material properties and Engineering Design parameters, General effects of processing on parameters, selection of structural, Electronic and Magnetic Materials – case studies.					08	
						Total	40
Text	 L.H. Van Vlack, <i>Elements of Materials Science & Engineering</i>, Addison-Wesley Publishing Company, New York. V Raghavan, <i>Materials Science & Engineering</i>, Prentice Hall of India Pvt. Ltd., New Delhi. 						
Reference	1. W D Callister, Jr., <i>Materials Science</i> of New York.	& Eng	ineer	ing –	An In	<i>troduction,</i> Johr	n Willey & Sons, Inc,