

## Presents an expert lecture on

## "Compact Modeling, Parameter Extraction and Verilog-A Implementation of RF MOS Devices: for 5G Applications"

Invited Speaker: Dr. Abhishek Kumar Upadhyay



Webinar Organized by: IIIT Bhagalpur

Date: 27<sup>th</sup> October, 2020 (Tuesday)

Time: 4:00 PM

G-meet Link: https://meet.google.com/giy-cmtf-ogd



Organizing Convener
Dr. Tameshwer Nath (Assistant Professor)
Department of Mechatronics Engineering
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Dr. Abhishek Kumar Upadhyay (Research Scientist Institute for Fundamentals of Electrical Engineering and Electro TU, Dresden, Germany