



Date: 31-08-2022

Advertisement for one Post of Junior Research Fellow (JRF)
in DST (SERB) Sponsored Project

Applications are invited from eligible candidates to work as Junior Research Fellow (JRF) on the project entitled “Development of ESD Protection Circuits/Devices for High Speed I/O Driver and RFICs” sponsored by DST-SERB (SRG/2021/001667). The position is purely on temporary basis, with an initial appointment for one year. However, it can be extended up to two years solely based on the performance of the candidate. Further, the candidate will be encouraged to apply in the Ph.D. program of the institute. The details are as follows:

01.	Principal Investigator (PI)	Dr. Dheeraj Kr. Sinha
02.	Essential Qualifications	B.E/B.Tech in Electronics & Communication Engineering or equivalent with minimum of 75% marks or 7.5 CGPA with valid GATE/NET, and/or M.E/M.Tech or Equivalent in the area of Microelectronics, VLSI, Embedded Systems with 75% marks or 7.5 CGPA from any recognized University/Institute.
03.	Desirable Qualifications	<ul style="list-style-type: none">• Good knowledge of device physics, analog/digital circuits, TCAD Device Simulation, Circuit Simulation.• Candidates with prior experience of working in these domains would be given preference.
04.	Age Limit	Not more than 32 years as on 10-09-2022.
05.	Salary/Emoluments	Rs. 31,000/- per month (consolidated) without HRA.
06.	Relaxation on qualification and age	SC/ST/OBC/WOMEN/PWD candidates will get relaxation as per Govt. of India Rules.
07.	No Objection Certificate	Employed candidates are to submit no objection certificate from his/her employer at the time of interview
08.	Date of Interview	The shortlisted candidates will be informed by e-mail about the written test and/or interview date. No TA/DA will be paid for attending the same.
09.	Others	As per DST norms.
10.	Last Date of google form Submission	10 th September 2022

Additional Information:

1. Preference will be given to the candidate who is having good academic record, research potential and relevant experience.
2. Interested candidates should apply via google form <https://forms.gle/ZbdhpYxeHnaE76fh6> on or before 10th September 2022.
3. The date of interview will be communicated for the shortlisted candidates by E-mail.
4. Candidates appearing for interview should come with one set of Xerox copies of certificates, mark sheets, and testimonials, along with their original documents.
5. Any queries related to above post must be addressed to: Dr. Dheeraj Kr. Sinha, Email: dksinha.ece@iiitbh.ac.in.
6. Selected candidates are expected to join immediately.