Curriculam Vitae

Dr. Arvind Choubey
Professor
Dept. of Electronics & Communication Engg.
National Institute of Technology, Jamshedpur

&

Coordinator
Indian Institute of Information Technology, Ranchi

1. International Exposure:

- **Chairman of two Sessions**: IEEE World Congress on Computational Intelligence, June 1- 6, 2008, **Hong Kong**
- **Chairman of the Session** of 2nd International Conference on Computer, Communication and Computational Sciences (IC4S 2017), Springer, October 11-12, Phuket,**Thailand**
- **Chairman of the Session** of 3rd IEEE-International Conference on Computational Intelligence & Communication Technology; CICT-2017, February 09-10, 2017, Ghaziabad, India
- Biodata publishedin **"Who's Who in the World"** in November 2009 publication**, U.S.A**
- Represented National Institute of Technology, Jamshedpur as an Event Faculty Advisor of the team DRIEFT, Project Formulae SAE from September 1-7, 2012 in **Japan**
- Presented research paper in IEEE SAI Intelligent System Conference, November 10-11, 2015, **London, UK.**
- Visited **Singapur** in June 2008
- **Keynote speaker** on 3rd IEEE-International Conference on Computational Intelligence & Communication Technology; CICT-2017, 9th February 2017, Ghaziabad, India
- **Best paper award** on "Quantum Behaved Particle Swarm Optimization Technique applied to FIR based Linear and Non-linear Channel Equalizer" in 2nd International Conference on Computer, Communication and Computational Sciences (IC4S 2017), Springer, October 11-12, Phuket,**Thailand**
- **Best paper award on** "Wind Driven Optimization Technique for Equalization of Non-minimum Phase Channel'' in International Conference on Sciences, Technology, Engineering & Management (ICSELM 2017), January 06-07, 2017, Kuala Lumpur, **Malaysia**

- **Advisory committee member**, 2nd International Conference on Science, Engineering, Law and Management (ICSELM 2017), June 01-02, 2017, **Mauritius**
- **Advisory committee member,** International Conference on Innovations in Science, Technology, Engineering and Management (ISTEM 2017), April 05-07, 2017,Chandigarh.

2. Responsibility in Central Government Organizations:

- Member, Executive Committee AICTE
- Expert Member of Interview Board for the Post of Professor in Union Public Service Commission (UPSC), New Delhi, Govt. of India.
- Expert Member of CSIR Assesment committee for Senior Scientist at Central Glass & Ceramics Research Institute under CSIR, Kolkata.
- Expert Member of CSIR Assessment committee for Senior Scientist at National Metlurgical Laboratory CSIR – Central Glass & Ceramics Research Institute, Kolkata.
- Expert Member, Campus Selection of Defence Research Development Organization (DRDO), Govt. of India.
- Expert Member of AICTE, Govt. of India to visit the Government Polytechnique, Andman and Nicobar.
- Project Co-coordinator of System to Chip Design (C2SD) under MHRD Project , Govt. of India
- Convener of Inspection committee for Institution of Jharkhand under Centre for Electronics Design and Technology (CEDTI), Govt. of India.
- Convener of Screening Committee for inspection of Institutions in Jharkhand & Bihar under DOEACC Society, Govt. of India

3. Responsibility in State Government Organizations:

- Expert Member of Interview Board for the selection of faculty post in Chhatishgarh Public Service Commission (CGPSC), Govt. of Chhatishgarh
- Expert Member of Interview Board for the selection of faculty post in Jharkhand Public Service Commission (JPSC), Govt. of Jharkhand.
- Expert Member of Inspection Team of various Technical and non Technical Institutions constituted by Vice-Chancellor, Kolhan University, Jharkhand.
- Member, Doctoral Advisory Committee, Sambalpur University, Orissa
- Evaluated Ph. D thesis of various State Universities

4. Administrative & Academic Experiences in NIT Jamshedpur:

- Coordinator, Indian Institute of Information Technology, Ranchi
- Member Senate,
- Head, Department of Electronics & Communication Engg.
- Chairman & Convener of various Enquiry Committee
- Chairman, Purchase Committee
- Chairman, UGPEC Committee
- Member, ACoFOR Committee
- Member, Computers and Networking Committee
- Member, UG Admission Committee
- Member, Pay Scale Committee
- Professor In-charge, Telephone & Internet
- Professor In-charge, Amateur Electronics Society
- Professor In-charge, Computer Society

5. Professional Affiliation:

- Member, Institute of Electrical and Electronics Engineers (IEEE), USA
- Fellow Member of Institute of Engineers (FIE), India
- Life Member of Indian Society for Technical Education (LISTE), India
- Reviewer of IEEE Antennas and Wireless Propagation Letters Journal
- Reviewer of International Journal of Engineering Science and Technology, Elsevier
- Reviewer of two Digital Signal Processing Books Published by Tata McGraw HILL
- Reviewer of " Digital Signal processing, A computer Based Approach, Third Edition" by Sanjit K Mitra, by Tata McGraw HILL
- Secretary cum Treasurer for Jharkhand & Bihar,Indian Society for Technical Education (ISTE),India
- Keynote speaker on January 12th, 2015 of National Workshop at Sambalpur University, Odisha, India
- Member, Program Committee of International Conference on Devices, Circuits and Communications (ICDCCom-2014), BIT Mesra, Ranchi
- Member, Programme Committee of International Conferenceon Advances in Computing, Communication and Control (ICAC3'15), Bandra, Mumbai, April

3-4, 2015.

- Member, Programme Committee of IEEE International Conference on Computer and Computational Sciences (ICCCS-2015), Greater Noida, UP, January 27-29, 2015.
- Member, Programme Committee of IEEE International Conference on Computer and Communications Technology (ICCCT-2014), NIT Allahabad, September 26-28, 2014
- Member, Programme Committee of International Conference on Computer and Computational Sciences (ICCCCS-2016), Aryabhatta College of Engineering & Research Center, Ajmer, August 12-13, 2016.

6. Teaching Experiences: UG & PG level –more than 30 years

- Coordinator, Indian Institute of Information Technology, Ranchi from 17 October 2017 to contd.
- Head, Dept. of Electronics & Communication Engg., NIT, Jamshedpur from Dec' 2000 to Jan' 2005 and May 2011 to May 2013.
- Professor, Department of Electronics & Communication Engg., NIT Jamshedpur, from April 2009 to contd.
- Associate Professor, Department of Electronics & Communication Engg., NIT Jamshedpur, from February 1997 to April 2009
- Assistant Professor, Department of Electrical Engg., NIT Jamshedpur, from May 1987 to February 1997
- Assistant Professor, Department of Electrical Engg. G.B. Pant Agriculture & Technical University, UP from August 1986 to April 1987

7. Academic Qualification:

General

Matriculation, BSSEB, Patna, 1974

I.Sc., UP Board, Allahabad,1976

B.Sc, Gorakhpur Univ., UP, 1979

Professional

B.Tech. Bihar University, Muzaffarpur, Electrical Engg.,1984

M. Tech. IIT BHU, Varanasi, Electrical Engineering, 1987

Ph.D Ranchi Univ., Electronics & Communication Engg., 2009

8. M. Tech. Dissertation:

"Design and application of Chopper Controller for the Speed Control of Slip Ring Induction Motor".

9. Ph.D Work:

"Development of New and Efficient Adaptive Channel Equalizers using Soft Computing Approach"

10. Research Experiences:

(a) Ph D Guidance: 06 Nos.

Sl. No	Students Name/Roll No.	Topic Of Thesis	Remarks
1.	Rashmi Sinha 2009RSEC003	Soft Computing Approach To Design A Novel Adaptive Equalizer for Linear and Non-Linear Channel	Awarded 2017
2.	Santosh Kumar Mahto (RS36/12)	Soft Computing Optimization Technique for Microwave and Millimeter Wave Applications	Awarded 2017
3.	GupteshwarSahu (RS18/12)	Time Frequency Transformation for Non Stationary Signal Analysis and Pattern Classification using Soft Computing	Thesis submitted Nov., 2017
4.	Prakash Ranjan (2014RSEC001)	Design and Development of Metasurface using Soft Computing Optimization Technique	To be submit March 2018
5.	Amit Prakash 2017RSEC001	Adaptive Filtering using Soft Computing Techniques	In Progress
6.	Chetan Barde 2017RSEC004	Soft Computing Optimization Technique for Substrate Integrated Waveguide Antenna Array Synthesis	In Progress

(b) M. Tech. Thesis Supervised: 23 Nos.

Sl. No	Year	Student Name/Roll No.	Supervisor	Topic of Dissertation
1.	2018	Suraj Kumar 2016PGECCO01	Dr. A. Choubey	Battery Swapping Technology and Manufacturing Swapping Station for Smaller Size LIP Battery to Support E-Vehicle
2.	2018	Praveen Kumar 2016PGECEM04	Dr. A. Choubey	Monople Antenna and its derivative for MIMO Applications

3.	2018	Kamal Hyder 2016PGECEM07	Dr. A. Choubey	Software Development and Analysis of Image Compression Algorithm
4.	2017	Manoj Kumar (2015PGECEM13)	Dr. A. Choubey	Digital metamaterial for microwave applications
5.	2017	Amit Anand (2015PGECEM02)	Dr. A. Choubey	Hardware implementation of Trigonometric Function
6.	2017	Satyashiba Sunder Jena (2015PGECCS13)	Dr. A. Choubey	Design of Smart Energy Meter
7.	2017	RupaliHongekar (2015PGECEM09)	Dr. A. Choubey	Design & Implementation of a Safety Path for eMotor Control using AURIX Automotive Microcontroller
8.	2016	VikashKumr Singh (2013PGECVE016)	Dr. A. Choubey	Design of low power and high speed full substractor using GDI logic on 180 nm technology.
9.	2016	Nirupam Dutta (2013PGECVE007)	Dr. A. Choubey	Design of low noise and low power phase lock loop in 180 nm technology.
10.	2015	DikshaKumari (2013PGECVE002)	Dr. A. Choubey	Constriction factor based particle swarm optimization for multi objective functions.
11.	2015	Sayandwip Sarkar (2013PGECVE014)	Dr. A. Choubey	Design an FPGA implementation of adaptive delta sigma modulator with enhanced SONR and dynamic range.
12.	2014	Sushmita Suman (1161121305)	Dr. A. Choubey & Mr. S. S. Thakur	Application of soft computing optimization technique in linear array antenna.
13.	2014	AnanatAnand Singh (El 412 009)	Dr. A. Choubey	Design optimization of fast unpowered SRAM cell.
14.	2014	Nishant Kumar Mishra	Dr. A. Choubey	Optimised delay performance of parallel prefix adder based on

		(El 412 077)		FPGA.
15.	2013	Phanendra Babu H (El 411 023)	Dr. A. Choubey	Design of decimation filter for audio band delta sigma ADC.
16.	2013	Mahendra M. Dhadwe (El 411 027)	Dr. A. Choubey	Module based implementation of partial reconfiguration in FPGA for counters.
17.	2013	Rambabu Kusuma	Dr. A. Choubey	Reduction of leakage power in CMOS MUX using leakage control transistor in 90nm technology
18.	2011	Mriganka Shekhar Sur(03/09)	Dr. A. Choubey	Design and implementation of 10-bit 60 SMPS.
19.	2008	Jayendra Kumar (JAM/03/E/PE/2000)	Dr. A. Choubey & Dr. S.S. Prasad	Design and development of microcontroller based remote control for home appliances.
20.	2002	Pradip Kumar Das (26/91 E)	Dr. A . Choubey & Dr. S. Agarwal	A novel approach to PID tuning practice for process control.
21.	1999	Susmita Sarkar (08/96)	Dr. A. Choubey & Dr. S.K. Mukherjee	Design development & analysis of closed loop DC drive.
22.	1997	Fakhruddin Ansari (14/97)	Dr. A. Choubey	Prediction of failure trends of electric mining shovels.
23.	1997	Jagdish Singh	Dr. A. Choubey	Generalised Implementation of Fuzzy Theory for a Chopper Controlled DC Drive

(c) B. Tech. Project Supervised: More than 50 projects

11. Participated Professional Practical Training:

- Thyristor Power Control Applications, organized by Department of Electrical Engineering, IT, B.H.U., Varanasi, December 08 – 28, 1986.
- Distributed Computer Control Systems, sponsored by MHRD, British Council

and IIT, Delhi, December 11-16, 1989.

- Recent Trends in Power Electronics and Industrial Control Organized by Department of Electrical Engineering, R.I.T (NIT), Jamshedpur, June 19-24, 1990.
- Energy Management, organized by Department of Electrical Engineering, IT, B.H.U., Varanasi, June 17-30, 1991.
- Recent trends in Electrical Machine and Drives, Organized by Electrical and Development Association at IIT. Bombay, January 17-18, 1992.
- Recent Advances in Power Generation from Non-Conventional Energy Sources, Organized by Department of Electrical Engineering, R.I.T (NIT)., Jamshedpur, May 25 - June 06, 1992.

- Application of Matlab-Simulink Software for Modelling and Simulation of System, Organized by Energy Centre, MANIT Bhopal, December 09 – 20, 2002.
- Evolutionary Computing & Advanced DSP, Organized by NIT Rourkela, June11-19, 2007.
- Soft Computing and Its Application, Organized by Dept. of CSE & IT, Andhra University, December 21 – 22, 2007

12. Seminar / Conference/ Workshop Organized:

- As a Convener, a National workshop organized on "VLSI and Embedded System Design" in Dept. of Electronics and Communication Engg., NIT Jamshedpur, from 6th - 8th March, 2009

13. Contribution to Teaching:

(a) Introduction / Design of New courses / Electives

- Regional Institute of Technology convertedintoNational Institute of Technology in Dec' 2002.Being Head of Department of Electronics & Communication Engg. from Dec' 2000 to Jan' 2005 the entire syllabus (Annual system to Semester system) was revised by consultation of Board of Study and IIT KGP Professors.
- Being a Co-coordinator of VLSI (SMDP II) of MHRD Project a PG course "VLSI and Embedded System" is introduced in the Dept. & started from session 2009-2010

(b) Laboratory Development

S.N	Details	
1	Power Electronics	In Feb' 1997 first time PE lab became operative when I joined as Asst. Prof. in Dept. of Electronics & Comm. Engg.

2	Digital Signal Processing	During my Headship DSP & FO lab were introduced & developed
3	Fiber Optics	

(c) Sponsored Projects / Research

S.N	Title of Project	PI/CO-PI	Funding Agency	Budget (Rs) Lakhs	Duration
i	SMDPII-VLSI: Special Manpower Development Programme in the area of VLSI	CO-PI	Ministry of Communications and Information Technology	Rs 60	five years + three years 2005-2011 & 2011-2014 (completed)
ii	Chip To System Design (C2SD)	CO-PI	Ministry of Communications and Information Technology	Rs 69.7	five years 15/12/2014-14/12/2019 (In Progress)
iii	Design of a Smart Array Antenna and its Application in Brain Cancer Detection	PI	National Institute of Technology , Jamshedpur	Rs 3.25	one year 2016-2017 (In Progress)

14. Paper Publications:

Book Chapters	**International Journals**	**National Journals**	**International Conferences**	**National Conferences**
04	14	03	12	03

(a) Book Chapter : 04

1. S. K. Mahto, **Arvind Choubey**, and S. Suman "Non-uniform Circular Array Antenna synthesis Using Wind Driven Optimization Algorithm," Microelectronics, Electromagnetics and Telecommunications Lecture Notes in Electrical Engineering, Springer, Vol. 372, pp 625-633, 2015,

2. G. Sahu, B. Biswal, **Arvind Choubey**, "Analysis of ECG Signals using Advanced Wavelet Filtering Approach," Advances in Intelligent Systems and Computing, Lecture Notes in Electrical Engineering, Springer, vol. 411, pp, 427-436, 2016.

3. R. Sinha, **Arvind Choubey**, S. K. Mahto, and P. Ranjan, "'Quantum Behaved Particle Swarm Optimization Technique applied to FIR based Linear and Non-linear Channel Equalizer" Advances in Intelligent Systems and Computing, Lecture Notes in Electrical Engineering, Springer, vol. , pp. , 2017.

4. S. K. Mahto, **Arvind Choubey**, R. Sinha, and P. Ranjan, ''Sidelobe minimization of Uniform Linear Array by Position and Amplitude-Only Control Using Wind Driven Optimization Technique'', Advances in Intelligent Systems and Computing, Lecture Notes in Electrical Engineering, Springer, vol. , pp. ,2017.

(b) International Journals: 14

1. Prakash Ranjan, Santosh Kumar Mahto, Arvind Choubey, "The Binary Wind Driven Optimization Algorithm and its Application in Antenna Array and Pixelated Metasurface Synthesis" IET Microwaves, Antennas & Propagation **(Under Review)**

2. Prakash Ranjan, Santosh Kumar Mahto, Arvind Choubey, "A six-band ultra-thin polarization-insensitive pixelated metamaterial absorber using a novel binary wind driven optimization algorithm" Journal of Computational Electronics, Springer **(Under Review)**

3. Rashmi Sinha, **Arvind Choubey**, "Soft Computing Techniques to Estimate FIR Filter Weights in an Adaptive Channel Equalizer: A Comparative Study" International Journal of Applied Engineering Research (IJAER) Vol. 13, pp. 3988-3995, 2017 **(SCOPUS Index)**

4. Prakash Ranjan, **Arvind Choubey**, Santosh Kumar Mahto, "Wide Angle Polarization Independent Multilayer Microwave Absorber using Wind Driven Optimization"Journal of Applied Engineering Research (IJAER) vol. 12 (19), pp. 8016-8025, 2017 **(SCOPUS Index)**

5. G. Sahu, **Arvind Choubey**, "Time-Frequency Analysis of Non-Stationary Waveforms in Power-Quality via Synchrosqueezing Transform" International Journal of Applied Engineering Research (IJAER) vol. 12 (13), pp.3714-3718, 2017 **(SCOPUS Index)**

6. Prakash Ranjan, **Arvind Choubey**, Santosh Kumar Mahto, "Optimal Design of multilayer wide band microwave absorber using Wind Driven Optimization Technique" AEU International Journal of Electronics and Communications, Elsevier, vol. 83, pp. 81-86, 2017**(SCI Index)**

7. **Arvind Choubey**, G. sahu, B. biswal : Analysis of ECG Signals Using Advanced Wavelet Filtering Approach - Advances in Intelligent Systems and Computing, Springer vol:411 pp: 427-436, 2016 (**SCI Index)**

8. Santosh Kumar Mahto, and **Arvind Choubey**, "A Novel Hybrid IWO/WDO Algorithm for Nulling Pattern Synthesis of Uniformly Spaced Linear and Non-uniform Circular Array Antenna", AEU International Journal of Electronics and Communications, Elsevier, 2016 **(SCI Index)**

9. Santosh Kumar Mahto, and **Arvind Choubey**, "A Novel Hybrid IWO/WDO Algorithm for Interference Minimization of Uniformly Excited Linear Sparse Array by Position-only Control", IEEE Antennas and Wireless Propagation Letters, Vol. 99, pp. 250-254, 2015, **(SCI Index)**

10. Anant Anand Singh, **Arvind Choubey** and Raj Kumar Maddheshiya, "Low Power SRAM Cell with Improved Response" International Journal of Research in Engineering and Technology, Vol. 3, Issue 06, pp. 483-488, 2014

11. Rambabu Kusuma, **Arvind Choubey** and Guguloth Sreekanth, "Reduction of leakage power in CMOS MUX using leakage control transistor in 90nm technology" International Journal of VLSI and Embedded Systems,Vol. 04, pp. 475-479, 2013

12. Phanendrababu H and **Arvind Choubey**, "Module Based Implementation of Partial Reconfiguration in FPGA for Counters" International Journal of Scientific &Technology Research vol. 2, issue 5, pp. 2010-2014, May 2013

13. Mahendra Dhadwe and **Arvind Choubey**, "Design of Multirate Linear Phase Decimation Filters for Oversampling ADC" International Journal of Engineering Research & Technology, Vol. 2, Issue 5, pp. 226-228, 2013

14. Jyoti Athiya, Rashmi Sinha, and**Arvind Choubey** "An Improved ECG SignalAcquisition System through CMOS Technology" International Journal of Engineering Science and Technology, Vol. 4, No.03, pp.1088-1094, 2012

(c) National Journal: 03

1. Rashmi Sinha, and **Arvind Choubey** "Design of a Discrete Adaptive Equalizer for Noisy Channel using Quantum Behaved Particle Swarm Optimization Technique", Indian Journal of Science and Technology, vol. 9, issue 41, pp. 1-8, 2016. **(Scopus Index).**
2. Rashmi Sinha, **Arvind Choubey**, and Santosh Kumar Mahto, "An Efficient Adaptive System Identification Technique based on Wind Driven Optimization Method", Indian Journal of Science and Technology, vol. 9, issue 38, pp. 1-6, 2016.**(Scopus Index).**
3. Rashmi Sinha, **Arvind Choubey**, and Santosh Kumar Mahto, "Wind Driven Optimization Technique for Equalization of Non-minimum Phase Channel."

Indian Journal of Science and Technology, vol. 10(16), pp. 1-11, 2017 **(Scopus Index).**

(d) International Conferences: 12

1. G. Sahu and **Arvind Choubey**, " Simultaneous Power-Quality Disturbances AnalysisUsing Modified S-Transform and Evolutionary Approach," International Conference on Micro-electronics, Electromagnetics and Telecommunications (ICMEET-2017), Springer, Hyderabad, India, Sept. 9-10, 2017.

2. Rashmi Sinha, and **Arvind Choubey**, "Adaptive Filtering Via Wind Driven Optimization Technique." IEEE International Conference On Computational Intelligence & Communication Technology (CICT-2017), Ghaziabad, 11-12, February 2017.

3. Rashmi Sinha, **Arvind Choubey**, and Santosh Kumar Mahto, "A Novel and Efficient Hybrid Least Mean Square (HLMS) Adaptive Algorithm for System Identification", IEEE Technically Co-Sponsored Science and Information (SAI) Intelligent System Conference 2015, London, UK, Nov. 10-11, 2015, pp. 894 - 897.

4. Santosh Kumar Mahto, **Arvind Choubey**, and R.Kumar , " A Novel Microstrip Antenna Using EBG Structure for Mobile and ISM Band," IEEE International Conferences at IIT Bhubaneswar, Dec 18-20, 2015

5. Santosh Kumar Mahto, **Arvind Choubey**, and R.Kumar , "A Novel Compact Multi-Band Double Y-Slot Microstrip Antenna Using EBG Structure," International Conference of Microwave and Photonics (ICMAP) ISM Dhanbad, pp.1-2, Dec 11-15, 2015

6. Santosh Kumar Mahto, **Arvind Choubey**, and S. Suman " Synthesizing Broad Null in Linear Array by Amplitude-only Control using wind driven optimization Technique", IEEE Technically Co-Sponsored Science and Information (SAI) Intelligent System Conference 2015, London, UK, Nov. 10-11, pp. 68-71, 2015

7. Rashmi Sinha, **Arvind Choubey**, and Santosh Kumar Mahto, "A Novel and Efficient Hybrid Least Mean Square (HLMS) Adaptive Algorithm for System Identification", IEEE Technically Co-Sponsored Science and Information (SAI) Intelligent System Conference 2015, London, UK, Nov. 10-11, pp. 894 - 897, 2015.

8. Sayandwip Sarkar, and **Arvind Choubey** "FPGA Implementation of All-Digital Adaptive Delta Sigma Modulator with Enhanced SQNR and Dynamic Range", IEEE International Conference on Electronics, Computing and Communication Technologies (CONECCT), Bangalore, India, July 10-11, 2015

9. Santosh Kumar Mahto, **Arvind Choubey**, and S. Suman " Linear Array synthesis with minimum side lobe level and null control using wind driven optimization", IEEE Signal Processing and Communication Engineering Systems (SPACES-2015), Vijayawada, AP, India, pp. 191-195, Jan.1-2, 2015

10. Majhi, R.; Panda, G.; Sahoo, G.; Panda, A.; **Arvind Choubey**. "Prediction of S&P 500 and DJIA stock indices using Particle Swarm Optimization technique,: IEEE World Congress on Computational Intelligence, June 1- 6, 2008, Hong Kong**,** pp. 1276 - 1282, 2008,

11. Majhi, B.; Panda, G.; **Arvind Choubey**. "Efficient scheme of pole-zero system identification using Particle Swarm Optimization technique", : IEEE World Congress on Computational Intelligence, June 1- 6, 2008, Hong Kong**,** pp. 446 - 451, 2008

12. Majhi, B.; Panda, G.; **Arvind Choubey**. "On the Development of a New Adaptive Channel Equalizer using Bacterial Foraging Optimization Technique" India Conference, 2006 Annual IEEE, pp. 1 - 6, 2006

(e) National Conferences: 03

1. **Arvind Choubey** and G. Sai Kiran, "An Overview of Bacterial Foraging Optimization for Adaptive System Identification and its applications", National Conference on Soft Computing Applications (NCSCA – 07), Visakhapatanam, 21-22 December, 2007

2. G. Panda, B. Majhi, D. Mohanty, **Arvind Choubey**and S. Mishra, "Development of Novel Digital Channel Equalisers using Genetic Algorithms", Proc. of National Conference on Communication (NCC-2006), IIT Delhi, pp. 117-121, 27-29, January, 2006.

3. G. Panda, Babita Majhi and **Arvind Choubey**, "Nonlinear Adaptive Channel Equalization using GA based Technique", Proc. of National Conference on Soft Computing and Machine Learning for Signal Processing, Control, Power and Telecommunications (NCSC-2006), Bhubaneswar, 24-26, March, 2006.

15.Familiarity with Software Packages:

- MatLab with Fuzzy, Neural Network and Simulink Tools
- MathCad, Pspice, Window XP, Ms-Office etc.

16.Country Visited:

- United Kiingdom (UK**),** Japan**,** Hong Kong, Singapore & Thailand

17. Personal:

Date of Birth : 5th January 1958

Father's Name : Late Ram Bilas Choubey

Address for correspondence

Official : Professor,
Dept. of Electronics & Communication Engg.
National Institute of Technology, Jamshedpur

831 014, Jharkhand, India
Ph. 0657-2374079 (O),+919431085066
Email: achoubey.ece@nitjsr.ac.in

Residential : 4D/58, Aditya Syndicate,
Adityapur-2,Jamshedpur- 831014
Jharkhand, India
M.+91 9113466792
Email:arvindnit@gmail.com

(Arvind Choubey)